

IN THE CLAIMS:

Please cancel claims 4-5, and 10-12 without prejudice, and amend claims 1, and 6-7 as follows:

1. (Currently Amended) A timing controller for a liquid-crystal display panel comprising:

a data enable signal detection circuit which detects a data enable signal applied to the timing controller, said data enable signal being transferred together with image data;
and

a timing generating circuit which controls a display timing of image data to be displayed on the liquid-crystal display panel including a start timing of display on the basis of the data enable signal detected by the data enable signal detection circuit and a clock externally supplied, said start timing of display being independent of horizontal and vertical synchronizing signals externally supplied.

2. (Original) The timing controller as claimed in claim 1, wherein the timing generating circuit comprises a first circuit which generates, from the data enable signal, a first start pulse which starts driving each data line of the liquid-crystal display panel,

and a second circuit which generates, from the data enable signal, a second start pulse which starts driving scanning lines of the liquid-crystal display panel.

3. (Original) The timing controller as claimed in claim 1, wherein the timing generating circuit comprises a circuit part which detects a beginning of each frame on the basis of the data enable signal.

4.–5. (Cancelled)

6. (Currently Amended) A method of controlling a display timing for a liquid-crystal display panel, the method comprising the steps of:

(a) detecting a data enable signal applied together with image data; and
(b) controlling the display timing of the image data to be displayed on the liquid-crystal display panel including a start timing of display on the basis of the data enable signal detected by the step (a) and a clock applied to the liquid-crystal display panel, said start timing of display being independent of horizontal and vertical synchronizing signals externally supplied.

7. (Currently Amended) A liquid-crystal display device comprising:
a liquid-crystal display panel having signal lines and scanning lines;

a data driver which drives the signal lines;

a gate driver which drives the scanning lines; and

a timing controller controlling a display timing of image data to be displayed on the liquid-crystal display panel,

the timing controller comprising:

a data enable signal detection circuit which detects a data enable signal applied to the timing controller; and

a timing generating circuit which controls the display timing on the basis of the data enable signal detected by the data enable signal detection circuit, said start timing of display being independent of horizontal and vertical synchronizing signals externally supplied.

8. (Original) The liquid-crystal display device as claimed in claim 7, wherein the timing generating circuit comprises a first circuit which generates, from the data enable signal, a first start pulse which starts driving each of the data lines, and a second circuit which generates, from the data enable signal, a second start pulse which starts driving the scanning lines.

9. (Original) The liquid-crystal display device as claimed in claim 7, wherein the timing generating circuit comprises a circuit part which detects a beginning of each frame on the basis of the data enable signal.

11.-12. (Cancelled)